



LV318281636

7
Amdt
B
8/14/03
ay

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/044,206
Filing Date January 11, 2002
Inventor Jerome Michael Eldridge
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Laura M. Schillinger
Attorney's Docket No. MI22-1914
Title: Methods of Forming Void Regions, Dielectric Regions and Capacitor
Constructions

RESPONSE TO MAY 12, 2003 OFFICE ACTION

To: Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

From: Satheesh K. Karra (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Responsive to the Office Action dated May 12, 2003, Applicant amends
and remarks as follows:

AMENDMENTS

RECEIVED
AUG - 6 2003
TECHNOLOGY CENTER 2800